Electronic Acknowledgement Receipt			
EFS ID:	1144220		
Application Number:	10719193		
Confirmation Number:	2999		
Title of Invention:	Integrated circuit with controllable test access to internal analog signal pads of an area array		
First Named Inventor:	Thaddeus John Gabara		
Correspondence Address:	Fyan, Mason & Lewis, LLP - 90 Forest Avenue - Locust Valley NY 11560 US 5167597517 -		
Filer:	Michael Lester Wise		
Filer Authorized By:			
Attorney Docket Number:	91-2-36		
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages		
1		1150-1188_AM4.PDF	214208	yes	6		
	Multipart Description						
	Doc Desc Amendment - After Non-Final Rejection Claims		Start	End			
			1	1			
			2	5			
	Applicant Arguments/Remarks	6	6				

Warnings:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.